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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/830,441	06/11/2001	Mohammed Javed Absar	851663.424US	8038

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EXAMINER

FLANDERS, ANDREW C

ART UNIT

PAPER NUMBER

2644

DATE MAILED: 12/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/830,441	ABSAR ET AL.	
	Examiner	Art Unit	
	Andrew C. Flanders	2644	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 September 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-29 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1, 2, 12, 13, 23, 24 and 27-29 is/are rejected.
- 7) ☒ Claim(s) 3-11, 14-22, 25 and 26 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 11 June 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Response to Arguments

Applicant's arguments filed 29 September 2005 have been fully considered but they are not persuasive.

Applicant Alleges:

"Paulos and Muwafi do not teach or suggest such computation stages. As noted by the Examiner, Paulos discloses a sampling rate converter that converts a digital signal having a first sampling rate to a digital signal having a second sampling rate. However, such a conversion of sampling rates does not imply a change in computation precision of arithmetic operations. As is known in the art, computation precision refers to the number of bits used to represent a value (See attached definition of "precision" from the Microsoft; Press Computing Dictionary). That is consistent with the use of precision in both the present application and in Muwafi. Paulos does not appear to mention any level of precision or otherwise imply a change in such precision."

Examiner respectfully disagrees with this allegation. While it may be true that computation precision refers to the number of bits used to represent a value, it is not the only possible definition of the term. Reading on the claims as broadly as possible, the term precision can also be defined as "Of or characterized by an accurate action" (See attached definition of "precision from The American Heritage College Dictionary"). In the instant case the first computational precision is the initial compact disc sampling rate. When sampling an analog signal into the digital domain, a precise measurement (or accurate action) is made at a given time. The number of measurements made is dependent upon the sampling rate. Thus changing the sampling rate changes the precision (i.e. computational precision). Thus the first sampling rate of 48KHz is a first

computational precision. The second sampling rate after conversion of the 48KHz would be the second level of computational precision. Thus the signal is sampled at one precision and then again at a second. As such, the argument is not persuasive and the rejection stands.

Applicant further alleges:

“Given that Paulos does not teach a change in computation precision, the combination of Paulos with Muwafi does not teach or suggest the invention recited in claim 1. Muwafi discloses an arithmetic manipulation unit (AMU) that has two operation modes: single precision mode and double precision mode. However, Muwafi does not suggest using both precision mode in a transform encoding process. Instead, Muwafi only suggests using either single precision mode or double precision mode - not both precision modes for the same digital data. thus, a hypothetical combination of Paulos with Muwafi would at best change sampling rates using the single precision mode or the double precision mode, but not both.”

Examiner respectfully disagrees with this allegation. First, as shown above, Paulos does teach a change in computation precision, thus the combination would as well. Further, Applicant states “Thus, a hypothetical combination of Paulos with Muwafi would at best change the sampling rates using the signal precision mode or the double precision mode, but not both.” Even if it were assumed that this statement were to be true, which the Examiner does not, the combination would still read upon the limitations set forth in the claims. Applicant admits that the sampling rates would be changed and thus, as shown above two levels of computation precision would be present. As such the argument is not persuasive and the rejection stands.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1, 2, 12, 13, 23, 24 and 27 – 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Paulos (U.S. Patent 6,208,671) in view of Muwafi (U.S. Patent 5,787,025).

Regarding **Claims 1, 12 and 23**, Paulos discloses:

A method of coding digital audio data (abstract) comprising:

a transform encoding process having plural levels of computation precision (i.e. sample rate conversion; abstract; sample rate conversion inherently takes one digital signal which has been previously encoded at a given sample rate and converts it into a second, different sample rate), wherein the transform encoding process includes:

a first computation stage involving arithmetic operations in transforming the digital audio signal data into intermediate audio data; using a first level of computational precision (i.e. a compact disc recording with a 48kHz sample rate; col. 1 lines 15 – 25, a compact disc recording with a 48kHz sampling rate inherently has had audio data transformed from the analog domain into the digital domain using a sampling process);

a second computation stage involving arithmetic operations in transforming the intermediate audio data into coded audio data using a second level of computation precision different than the first level of computational precision (i.e. a sample rate converter converts a digital signal having a first sample rate to a substantially similar digital signal having a second sample rate; col. 1 lines 12 – 15);

Paulos doesn't explicitly disclose wherein the transform encoding process is in accordance with the AC-3 Digital Audio Compression Standard or implementing this process on a fixed point digital signal processor.

However, Paulos discloses that AC-3 audio may have sample rates of 44.1 kHz; col. 1 lines 20 - 25. It would have been obvious to one of ordinary skill in the art at the time of the invention to use Paulos' sample rate converter to convert the CD audio at 48 kHz into AC-3 audio at 44.1 kHz. One would have been motivated to do so to allow the seamless processing of two digital signals operating at two different sampling rates; Paulos col. 1 lines 15 – 17.

Furthermore, the modification of Paulos does not explicitly disclose implementing this process on a fixed point digital signal processor.

Muwafi discloses implementing this process on a fixed point digital signal processor (i.e. a signal processing circuit for performing single and double precision operations; col. 1 lines 5 – 15; in which the invention is desirably implemented in a DSP for use in communications operations which received digitized audio; col. 3 lines 1 – 7).

It would have been obvious to one of ordinary skill in the art to implement the modification of Paulo on a digital signal processor such as the one disclosed by Muwafi.

One would have been motivated to do so to more efficiently processes the audio signals; see Muwafi col. 3 lines 36 – 53.

Regarding **Claims 2, 13 and 24**, in addition to the elements stated above regarding claims 1 , 12 and 23, the combination of Paulos in view of Muwafi further discloses:

wherein the digital signal processor comprises a 16-bit digital signal processor which is capable of single (16-bit) precision computations and double (32-bit) computations (i.e. the circuit operates in single or double precision mode; col. 1 lines 5 – 15).

Regarding **Claims 27, 28 and 29**, in addition to the elements stated above regarding claims 1, 12 and 23, the combination of Paulos in view of Muwafi further discloses:

wherein the first level of computational precision is performed using data elements of a first number of bits and the second level of computational precision is performed using data elements of a second number of bits (i.e. two different sampling rates using the same quantization level would have a different number of bits because the signal is sampled more often and thus requires more bits to describe the signal).

Allowable Subject Matter

Claims 3 – 11, 14 – 22, and 25 - 26 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew C. Flanders whose telephone number is (571) 272-7516. The examiner can normally be reached on M-F 8:30 - 5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Vivian Chin can be reached on (571) 272-7848. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2644

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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